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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/750,908	01/05/2004	David Pincu	PDS-012-US3	1999
39933 75	90 06/26/2006		EXAM	INER
POWERDSINE LTD.			KAPLAN, HAL IRA	
C/O LANDON	IP, INC			
1700 DIAGON	AL ROAD, SUITE 450	0	ART UNIT	PAPER NUMBER
ALEXANDRIA	A, VA 22314-2866		2836	

DATE MAILED: 06/26/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)	L
	10/750,908	PINCU ET AL.	
Office Action Summary	Examiner	Art Unit	
	Hal I. Kaplan	2836	
The MAILING DATE of this communication	appears on the cover sheet v	vith the correspondence address	
eriod for Reply	501 V 10 05T TO 5VDIDE - 1	40NT(1/0) OD T(11DT)/ (00) DA	
A SHORTENED STATUTORY PERIOD FOR REWHICHEVER IS LONGER, FROM THE MAILING. Extensions of time may be available under the provisions of 37 CF after SIX (6) MONTHS from the mailing date of this communication. If NO period for reply is specified above, the maximum statutory properties of the period for reply within the set or extended period for reply will, by some Any reply received by the Office later than three months after the rearned patent term adjustment. See 37 CFR 1.704(b).	G DATE OF THIS COMMUN FR 1.136(a). In no event, however, may a n. eriod will apply and will expire SIX (6) MO statute, cause the application to become a	ICATION. reply be timely filed NTHS from the mailing date of this communic BANDONED (35 U.S.C. § 133).	
tatus			
1) Responsive to communication(s) filed on 6	05 January 2004.		
_	This action is non-final.		
3) Since this application is in condition for all	owance except for formal ma	tters, prosecution as to the meri	ts is
closed in accordance with the practice und	der <i>Ex par</i> te Quayle, 1935 C.	D. 11, 453 O.G. 213.	
Disposition of Claims			
4) Claim(s) <u>1-40</u> is/are pending in the applica 4a) Of the above claim(s) is/are with			
5)⊠ Claim(s) <u>1-19 and 21-40</u> is/are allowed.		•	
6) Claim(s) is/are rejected.			
7)⊠ Claim(s) <u>20</u> is/are objected to.			
8) Claim(s) are subject to restriction a	nd/or election requirement.		
application Papers			
9)⊠ The specification is objected to by the Exar	miner.		
10)⊠ The drawing(s) filed on <u>05 January 2004</u> is	a/are: a) ☐ accepted or b) ⊠	objected to by the Examiner.	
Applicant may not request that any objection to	the drawing(s) be held in abeya	ance. See 37 CFR 1.85(a).	
Replacement drawing sheet(s) including the co	•	• , , •	* *
11)☐ The oath or declaration is objected to by th	e Examiner. Note the attache	ed Office Action or form PTO-15	2.
riority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for for a) All b) Some * c) None of: 1. Certified copies of the priority docur		§ 119(a)-(d) or (f).	
 Certified copies of the priority docur Certified copies of the priority docur 		Application No	
3. Copies of the certified copies of the		· ·	e
application from the International Bu	·		
* See the attached detailed Office action for a	, , , , , , , , , , , , , , , , , , , ,	t received.	•
ttachment(s) Notice of References Cited (PTO-892)	4) 🗆 Interview	Summary (PTO-413)	
) Notice of Draftsperson's Patent Drawing Review (PTO-948			
) 🔯 Information Disclosure Statement(s) (PTO-1449 or PTO/S		o(s)/Mail Date Informal Patent Application (PTO-152)	

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DETAILED ACTION

Specification

1. The disclosure is objected to because of the following informalities: Paragraph 12, lines 1-2, the end of the sentence is omitted. Paragraph 213, line 11 contains the phrase "power of Ethernet". It appears this should be "power over Ethernet".

Paragraph 254, line 4 contains the phrase "1104, 1106 and 240". It appears this should be "1104, 1106 and 1240". Paragraph 287, line 2 contains the phrase "SIUs 300". It appears this should be "SIUs 1300". Paragraph 426, lines 5-6 contain the phrase "current sensor 1840". It appears this should be "current sensor 1580". Paragraph 469, lines 9-10 contain the phrase "demand from of electrical load of the node". This is not proper idiomatic English, and it is not clear what this means.

Appropriate correction is required.

2. The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

Drawings

3. The drawings are objected to because of the following informalities: Figures 18A, 18B, 19A, and 19B contain the phrase "Ethernet Switch with POL". It appears this should be "Ethernet Switch with POE". In Figures 22A, 22B, 24A, and 24B, filter 1830 does not contain the correct electrical symbol for a capacitor. A battery is shown. Figure 23A contains the phrase "to/from PSC 340,345". It appears this should be

"to/from PSC 1340,1345". Paragraph 431, lines 10-13 state that possible modifications of the voltage current relationship are represented by various curves drawn in dashed lines in Figure 26A. These curves do not appear in Figure 26A. Figure 27 contains the phrase "to/from power bus 210". It appears this should be "to/from power bus 1210".

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- 4. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(4) because reference character "1026" has been used to designate both the printer and the computer in Figure 16; reference character "1104" has been used to designate both the file server and the Ethernet switch in Figure 18A; reference character "1450" has been used to designate both SIUs and OPCs in Figure 19A; reference character "1350" has been used to designate both the IP phone gateway internal bus and the gateway circuitry in Figure 19B; reference character "3360" has been used to designate both the "interrogate PSC parameters" and "are results ok" stages in Figure 31; and reference character "3920" has been used to designate both "accept assigned node" stages in Figure 35.
- 5. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they do not include the following reference sign(s) mentioned in the description: 238 in Figure 2; 338 in Figure 3; 538 in Figure 5; 840 in Figure 8; 990 in Figure 10; 1028 in Figure 16; 1260 in Figure 18A; 1400 in Figure 19B; 1330 in Figure 25A; and 3940 in Figure 35.
- 6. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they include the following reference character(s) not mentioned in the description: 1338 in Figure 13B; 1375 in Figure 18A; 912 and 1376 in Figure 18B; 1033

in Figure 19B; 1333 in Figure 27C; and V_{NOM} in Figure 28A. Corrected drawing sheets in compliance with 37 CFR 1.121(d), or amendment to the specification to add the reference character(s) in the description in compliance with 37 CFR 1.121(b) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. Each drawing sheet submitted after the filling date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Objections

7. Claim 20 is objected to because of the following informalities: Claim 20, lines 1-2 contain the phrase "at least one of said DC electrical power interconnections". It appears this should be "said DC electrical power interconnections", as the arrangement applies to the interconnections as a whole. Appropriate correction is required.

Allowable Subject Matter

- 8. Claims 1-19 and 21-40 allowed.
- 9. The following is an examiner's statement of reasons for allowance:

Claims 1-19 and 21-40 are allowed because none of the prior art of record discloses or suggests a plurality of Ethernet nodes each having a first operative mode in which it may provide more power than it consumes and a second operative mode in

which it may consume more power than it provides, in combination with the remaining claimed features.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

- 10. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The US patent application publication of Wilson et al. (2003/0187550) discloses a similar system.
- 11. This application is in condition for allowance except for the following formal matters:

The disclosure is objected to because of the following informalities: Paragraph 12, lines 1-2, the end of the sentence is omitted. Paragraph 213, line 11 contains the phrase "power of Ethernet". It appears this should be "power over Ethernet".

Paragraph 254, line 4 contains the phrase "1104, 1106 and 240". It appears this should be "1104, 1106 and 1240". Paragraph 287, line 2 contains the phrase "SIUs 300". It appears this should be "SIUs 1300". Paragraph 426, lines 5-6 contain the phrase "control signal 1840". It appears this should be "control signal 1580". Paragraph 469, lines 9-10 contain the phrase "demand from of electrical load of the node". This is not proper idiomatic English, and it is not clear what this means.

The drawings are objected to because of the following informalities: Figures 18A, 18B, 19A, and 19B contain the phrase "Ethernet Switch with POL". It appears this should be "Ethernet Switch with POE". Figure 23A contains the phrase "to/from PSC 340,345". It appears this should be "to/from PSC 1340,1345". Paragraph 431, lines 10-13 state that possible modifications of the voltage current relationship are represented by various curves drawn in dashed lines in Figure 26A. These curves do not appear in Figure 26A. Figure 27 contains the phrase "to/from power bus 210". It appears this should be "to/from power bus 1210".

The drawings are objected to as failing to comply with 37 CFR 1.84(p)(4) because reference character "1026" has been used to designate both the printer and the computer in Figure 16; reference character "1104" has been used to designate both the file server and the Ethernet switch in Figure 18A; reference character "1450" has been used to designate both SIUs and OPCs in Figure 19A; reference character "1350" has been used to designate both the IP phone gateway internal bus and the gateway circuitry in Figure 19B; reference character "3360" has been used to designate both the "interrogate PSC parameters" and "are results ok" stages in Figure 31; and reference character "3920" has been used to designate both "accept assigned node" stages in Figure 35.

The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they do not include the following reference sign(s) mentioned in the description: 238 in Figure 2; 338 in Figure 3; 538 in Figure 5; 840 in Figure 8; 990 in

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Figure 10; 1028 in Figure 16; 1260 in Figure 18A; 1400 in Figure 19B; 1330 in Figure 25A; and 3940 in Figure 35.

The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they include the following reference character(s) not mentioned in the description: 1338 in Figure 13B; 1375 in Figure 18A; 912 and 1376 in Figure 18B; 1033 in Figure 19B; 1333 in Figure 27C; and V_{NOM} in Figure 28A.

Claim 20 is objected to because of the following informalities: Claim 20, lines 1-2 contain the phrase "at least one of said DC electrical power interconnections". It appears this should be "said DC electrical power interconnections", as the arrangement applies to the interconnections as a whole.

Prosecution on the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.

A shortened statutory period for reply to this action is set to expire **TWO**MONTHS from the mailing date of this letter.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hal I. Kaplan whose telephone number is 571-272-8587. The examiner can normally be reached on M-F 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on 571-272-2800 x36. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

hik

rubert L. Debenadiris Primary examiner